*Roll No. ............................*

# OLE-24487

## B. Tech. 7th Semester (CSE)
## Examination – April, 2021
### ADVANCED COMPUTER ARCHITECTURE
**Paper : CSE-401-F**

*Time : Three hours ]*       *[Maximum Marks : 100*

*Before answering the questions, candidates should ensure that they have been supplied the correct and complete question paper. No complaint in this regard, will be entertained after examination.*

***Note :*** Attempt any *five* questions in all, selecting *one* from each Unit. Question No. **1** is *compulsory*.

1. Write short notes on following :      20
   (a) Mircroprogramming
   (b) Cache Memory
   (c) Physical Memory
   (d) Vector Processor

### UNIT – I

2. Explain the following :      20
   (i) Instruction and Instruction Set
   (ii) Virtual to Real Mapping

3. Describe the statement :      20

   "The Economics of a Processor Project : A Study"

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## UNIT – II

4. What do you mean by Cache Organization ? Also describe write policies in detail.                    20

5. Explain the following :                    20
   (i) Virtual to Real Translation
   (ii) Overlapping in the T cycle in V-R translation

## UNIT – III

6. Describe open, closed and mixed queue models in detail.                    20

7. Explain the following :                    20
   (i) Physical Memory
   (ii) Processors with Cache

## UNIT – IV

8. Give a complete description about concurrent processors.                    20

9. Describe shared memory multi processors and its types in detail.                    20

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